change to the area of the first display.

2.   (Amended) A control system according to claim 1 [characterised in that if the Vsync signal has occurred the] wherein said generation, addition or other alteration with regard to the part of [the OSD] said on-screen display occurs immediately when the vertical synchronization signal has occured[, whereas if no Vsync signal has occurred then the control means delays the new operation until the Vsync signal has occurred].

3.   (Amended) A control system according to claim 1 [characterised in that if the display data buffer memory of a part of the first display is changed, the] wherein said system continues until a request to draw into the previously displayed data buffer of [the] a part of the first display is received when the display data buffer memory of the part of the first display is changed.[, at which point, if a Vsync signal has occurred in the intervening period of time the generation of said second display occurs.]

4.   (Amended) A control system according to claim 1 [characterised in that if a part of the first display is moved, the] wherein said system continues when part of the first display is moved until a request to draw into its display data buffer memory is received. [at which time the processing proceeds immediately if a Vsync signal has occurred since the movement of the part of the first display, otherwise the command to redraw the display waits for a Vsync signal to occur.]

5.   (Amended) A system according to claim 1 [characterised in that Typically, if a region

of the first OSD is deleted, the] <u>wherein said</u> system continues in operation until a request to create a new region is made <u>when a region of the first on-screen display is deleted.</u> [and at that time, if a vsync signal has occurred since the deletion, the creation can take place immediately but, if not, the creation is delayed until the vertical sync signal occurs.]

6.   (Amended) A system according to claim 1 [characterised in that the] <u>wherein said</u> system is controlled with regard to the occurrence of the [vsync] <u>vertical synchronization</u> signal with respect to those changes in [the OSD] <u>said on-screen display</u> which would not cause an artefact to be [crated] <u>created</u> on screen.

7.   (Amended) A system according to claim 1 [characterised in that] <u>wherein</u> when the request for an alteration is made, the first [OSD] <u>said on-screen display</u> display continues to be displayed until the generation of the change occurs.

8.   (New) A control system according to claim 1 wherein said control means delays a new operation until the vertical synchronization signal has occurred.

9.   (New) A control system according to claim 3 wherein the generation of a second display occurs when a vertical synchronization signal has occurred in the intervening period of time.

10.   (New) A control system according to claim 4 wherein processing proceeds immediately when a vertical synchronization signal has occurred since the movement of the part of the first

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1.   (Amended) A system for the control of the generation of an on-screen display on a display screen, said system comprising:

control means for the display continue to operate the system until a request to generate, add or otherwise alter the display of an area is received upon the deletion, change or movement of an area of a first on screen display whereupon the control means detects whether or not a vertical synchronization signal for the display screen has occurred since the change to the area of the first display.

2.   (Amended) A control system according to claim 1 wherein said generation, addition or other alteration with regard to the part of said on-screen display occurs immediately when the vertical synchronization signal has occurred.

3.   (Amended) A control system according to claim 1 wherein said system continues until a request to draw into the previously displayed data buffer of a part of the first display is received when the display data buffer memory of the part of the first display is changed.

4.   (Amended) A control system according to claim 1 wherein said system continues when part of the first display is moved until a request to draw into its display data buffer memory is received.

5.   (Amended) A system according to claim 1 wherein said system continues in operation until a request to create a new region is made when a region of the first on-screen display is deleted.

6.   (Amended) A system according to claim 1 wherein said system is controlled with regard to the occurrence of the vertical synchronization signal with respect to those changes in said on-screen display which would not cause an artefact to be created on screen.

7.   (Amended) A system according to claim 1 wherein when the request for an alteration is made, the first said on-screen display display continues to be displayed until the generation of the change occurs.

8.   (New) A control system according to claim 1 wherein said control means delays a new operation until said vertical synchronization signal has occurred.

9.   (New) A control system according to claim 3 wherein the generation of a second display occurs when a vertical synchronization signal has occurred in the intervening period of time.

10.   (New) A control system according to claim 4 wherein processing proceeds immediately when a vertical synchronization signal has occurred since the

movement of the part of the first display, otherwise the command to redraw the display waits for a vertical synchronization signal to occur.

11.   (New) A system according to claim 5 wherein said creation may occur immediately when the vertical synchronization signal had occurred since the deletion.

12.   (New) A system according to clam 5 wherein the creation is delayed until the vertical synchronization signal occurs.